

# **Notice of References Cited**

Application No.  
09/092,115

Applicant(s)

Hanratty et al.

Examiner

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## **U.S. PATENT DOCUMENTS**

	DOCUMENT NO.	DATE	NAME	CLASS	SUBCLASS
A	5,139,904	8/18/92	Auda et al.	----	----
B	5,525,542	6/11/96	Maniar et al.	----	----
C	5,798,555	8/2/98	Mishra et al.	----	----
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G					
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## **FOREIGN PATENT DOCUMENTS**

	DOCUMENT NO.	DATE	COUNTRY	NAME	CLASS	SUBCLASS
N						
O						
P						
Q						
R						
S						
T						

## **NON-PATENT DOCUMENTS**

	DOCUMENT (Including Author, Title, Source, and Pertinent Pages)	DATE
U	Wolf et al., Silicon Processing for the VLSI Era, VII, pp.278-286	1990
V		
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